

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,148,570 B2  
APPLICATION NO. : 09/928975  
DATED : December 12, 2006  
INVENTOR(S) : Scott Brad Herner et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Claims

Column 8, in claim 2, line 1, before "of claim 1" delete "invention" and substitute --semiconductor structure-- in its place.

Column 8, in claim 3, line 1, before "of claim 1" delete "invention" and substitute --semiconductor structure-- in its place.

Column 8, in claim 4, line 1, before "of claim 1" delete "invention" and substitute --semiconductor structure-- in its place.

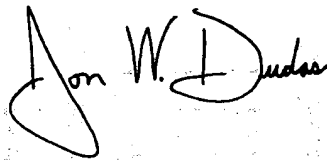
Column 8, in claim 5, line 1, before "of claim 1" delete "invention" and substitute --semiconductor structure-- in its place.

Column 8, in claim 6, line 1, before "of claim 1 or 5" delete "invention" and substitute --semiconductor structure-- in its place.

Column 8, in claim 7, line 9, delete "squarel" and substitute --square-- in its place.

Signed and Sealed this

Third Day of April, 2007

A handwritten signature in black ink, reading "Jon W. Dudas". The signature is stylized with a large, looping initial "J" and a distinct "D".

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*